

Application no. 09/620,544
Amdt. Dated August 27, 2004
Reply to Final Office Action of June 30, 2004

Amendments To the Drawings:

The attached sheet of drawings includes changes to Fig. 15. This sheet replaces the original sheet. In Fig. 15, LBA5 in row 716 is amended to marked with a "1".

Attachment: Replacement Sheet

Annotated Sheet Showing Changes

REMARKS/ARGUMENTS

A new PTO – 1449 is being submitted herewith to properly indicate the inventors' names as identification of the references A1 – A157 in accordance with MPEP 609. Some of the previously identified references are being removed. Documents B1 – B45 of the February, 2004 IDS now appear identifying the foreign patent or published foreign patent application by country or patent office which issued the patent or published application. It is therefore respectfully requested that the IDS of February 27, 2004 be considered accordingly.

In amended Fig. 15, LBA5 in row 716 is amended to marked with a "1".

Claims 2-17 remain in the subject application. Claims 2 – 13 and 15 -17 are amended, as recited hereinabove.

Claims 2-17 have been rejected under 35 U.S.C. 102(e) as being allegedly anticipated by Hasbun et al. (5,586,285). In the claimed invention, there is recited "one or more blocks caused to be identified by a predetermined group of logical block addresses corresponding to a predetermined group of sectors ...", i.e. in the present invention, the logical block addresses correspond to predetermined groups of one or more blocks and do not change, as in Hasbun et al. In Hasbun et al., it is believed that the LSNs corresponding to a block change, if not every time, at least some times, when a new write operation occurs. That is, in Hasbun et al., when the host issues a write command, a block having a different logical block address than that used for the previous version of the sector is assigned for storage of the modified sectors. Thus, Hasbun et al. does not teach, "one or more blocks caused to be identified by a predetermined group of logical block addresses corresponding to a predetermined group of sectors".

Additionally, Hasbun et al. is not believed to disclose "identification of the one or more new blocks by the same predetermined group of logical block addresses", as does the claimed invention [emphasis added].

It is therefore believed that independent claims 2, 3, 7, 11, 15 -17 are patentable over Hasbun et al. and all claims depending therefrom are also necessarily patentable over Hasbun et al.

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Reconsideration and allowance of claims 2-17 is hereby respectfully requested.

Applicants submit that the subject application is now in condition for allowance and an early notice thereof is respectfully requested. Should any further amendment be required prior to passing the application to issue, the Examiner is respectfully invited to contact the undersigned by telephone at the number set out below.

Respectfully submitted,
LAW OFFICES OF IMAM

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Attachments

I hereby certify that this correspondence with all attachments is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Arlington VA 22313-1450 on August 27, 2004 by Erika Villafana.

August 27, 2004


Erika Villafana